



R.M.K. COLLEGE OF ENGINEERING AND TECHNOLOGY

RSM Nagar, Puduvoyal- 601206, Gummidipoondi (T.K), Thiruvallur (D.T), Tamil Nadu

(An Autonomous Institution)

Approved by AICTE, New Delhi/ Affiliated to Anna University, Chennai

Accredited by NBA (All Eligible Courses) / NAAC with "A" GRADE

An ISO 21001:2018 Certified Institution



FACULTY PROFILE

PERSONAL INFORMATION		
Name	:	Dr.K.Sangeethalakshmi
Designation	:	ASSISTANT PROFESSOR-II
Department	:	ECE
DOB	:	28.08.1980
DOJ	:	29.06.2011
AICTE Unique ID	:	1752392594
Anna University Faculty ID	:	269665
Email	:	Sangeetha.lk@rmkcet.ac.in
Phone	:	04467900640



EDUCATIONAL QUALIFICATION			
UG	PG	DOCTORATE	OTHER QUALIFICATION
B.E (ECE)	M.E (VLSI design)	(Ph.D)	-
Title of Ph.D.			
Design of Miniaturized Frequency Selective Surface for EMI Shielding			
Areas of Expertise & Research Interests			
EMI Shielding, Wireless Communication, VLSI Design			

WORK EXPERIENCE			
Teaching	Research	Industry	Total
13Years 8 Months	7	2 Years 4 Months	16Years

RESEARCH GUIDANCE

Anna University Supervisor Recognition Number:			
Ph.D Awarded	-	Ph.D Pursuing	-

RESEARCH & PUBLICATIONS			
Journal Publications (SCI/Scopus/UGC-Care Indexed)	7	Scopus Citation	149
Conference Publications (Scopus Indexed)	13	Google Scholar Citation	218
Book Chapters (Scopus Indexed)	-	Scopus h index	6
Others	12	Google Scholar h index	7
Total publications	32	Text Books Authored	-

PATENTS, INNOVATIONS & INTELLECTUAL PROPERTY			
Granted Patents:	-	Filed Patents	2
Technology Transfers & Commercialized Products	-	Total	2

SPONSORED RESEARCH PROJECTS, GRANTS & CONSULTANCY					
Project Title	Funding Agency/Industry	Grant Amount	Duration	Role (PI/Co-PI)	Status
-	-	-	-	-	-

PROFESSIONAL MEMBERSHIPS & AFFILIATIONS			
Professional Society Member	Editorial Board Member	Editor	Reviewer
4	-	-	-

CONFERENCES, WORKSHOPS, & SEMINARS

Attended	Organized	Invited Talks / Guest Lectures delivered
28	-	-

TEACHING & UP SKILLING		
YouTube Lectures Link	e-Content Link	MOOC Courses Completed
https://www.youtube.com/channel/UCrCP3YRRdCkj1zOdKhWAjiw	-	13

ADMINISTRATIVE RESPONSIBILITIES		
Institution Level	Course Coordinator <ul style="list-style-type: none">• VLSI Design• RTL Design with VHDL/Verilog HDL• FPGA Architectre and Applications	
Department Level	<ul style="list-style-type: none">• RMK NEXTGEN Coordinator• Miniproject Coordinator• Department Overall Lab Coordinator• Criteria 6 coordinator• Counsellor/Mentor• VLSI - COE Coordinator• VLSI LAB In-charge• Project Coordinator	
AWARDS & RECOGNITIONS		
Name of Award	Institution/Organization	Year
NPTEL BELIEVER	NPTEL	2024
BEST FACULTY	INSO	2023
Additional Information [Any other relevant details]		